CLAIMS

What is claimed is:

1. A processor, comprising:

a predicate predictor to determine a predicted predicate value and a confidence value for a first instruction with a predicate; and

a micro-op generator to issue a first set of micro-ops corresponding to said first instruction when said confidence value is high and a second set of micro-ops corresponding to said first instruction when said confidence value is low.

2. The processor of claim 1, wherein said first set of micro-ops includes a check micro-op.

3. The processor of claim 2, wherein said check micro-op is to check for a calculated value of said predicate of true when said predicted predicate value is true.

4. The processor of claim 3, wherein said check micro-op is to initiate a recovery when said calculated value is false.

5. The processor of claim 3, wherein said first set of micro-ops includes a first micro-op corresponding to said first instruction without predicate.

6. The processor of claim 2, wherein said check micro-op is to check for a calculated value of said predicate of false when said predicted predicate value is false.

7. The processor of claim 6, wherein said check micro-op is to initiate a recovery when said calculated value is true.

8. The processor of claim 1, wherein said second set of micro-ops includes a micro-op corresponding to said first instruction without predicate.

9. The processor of claim 8, wherein said second set of micro-ops includes a conditional move micro-op.

10. A method, comprising:

determining a predicted predicate value for a first instruction with a predicate;

determining a confidence value for said predicted predicate value; and

issuing a set of micro-ops corresponding to said first instruction responsive to said confidence value.

11. The method of claim 10, wherein said set of micro-ops includes a check micro-op when said confidence value is high.

12. The method of claim 11, wherein said check micro-op checks for a calculated value of said predicate of true when said predicted predicate value is true.

13. The method of claim 12, further comprising initiating a recovery when said calculated value of said predicate is false.

14. The method of claim 12, further comprising issuing a first micro-op corresponding to said instruction without predicate.

15. The method of claim 11, wherein said check micro-op checks for a calculated value of said predicate of true when said predicted predicate value is false.

16. The method of claim 15, further comprising initiating a recovery when said calculated value of said predicate is true.

17. The method of claim 10, wherein said set of micro-ops includes a conditional move micro-op when said confidence value is low.

18. A system, comprising:

a processor including a predicate predictor to determine a predicted predicate value and a confidence value for a first instruction with a predicate, and a micro-op generator to issue a first set of micro-ops corresponding to said first instruction when said confidence value

is high and a second set of micro-ops corresponding to said first instruction when said confidence value is low;

an interface to couple said processor to input-output devices; and

an audio input-output coupled to said interface and said processor.

19. The system of claim 18, wherein said first set of micro-ops includes a check micro-op.

20. The system of claim 19, wherein said check micro-op is to check for a calculated value of said predicate of true when said predicted predicate value is true.

21. The system of claim 20, wherein said check micro-op is to initiate a recovery when said calculated value is false.

22. The system of claim 21, wherein said first set of micro-ops includes a first micro-op corresponding to said first instruction without predicate.

23. The system of claim 19, wherein said check micro-op is to check for a calculated value of said predicate of false when said predicted predicate value is false.

24. The system of claim 23, wherein said check micro-op is to initiate a recovery when said calculated value is true.

25. The system of claim 18, wherein said second set of micro-ops includes a micro-op corresponding to said first instruction without predicate.

26. The system of claim 25, wherein said second set of micro-ops includes a conditional move micro-op.

27. An apparatus, comprising:

means for determining a predicted predicate value for a first instruction with a predicate;

means for determining a confidence value for said predicted predicate value; and

means for issuing a set of micro-ops corresponding to said first instruction responsive to said confidence value.

28. The apparatus of claim 27, wherein said set of micro-ops includes a check micro-op when said confidence value is high.

29. The apparatus of claim 28, wherein said check micro-op checks for a calculated value of said predicate of true when said predicted predicate value is true.

30. The apparatus of claim 29, further comprising means for initiating a recovery when said calculated value of said predicate is false.

31. The apparatus of claim 30, further comprising means for issuing a first micro-op corresponding to said instruction without predicate.

32. The apparatus of claim 28, wherein said check micro-op checks for a calculated value of said predicate of true when said predicted predicate value is false.

33. The apparatus of claim 32, further comprising means for initiating a recovery when said calculated value of said predicate is true.

34. The apparatus of claim 27, wherein said set of micro-ops includes a conditional move micro-op when said confidence value is low.